## **COEN 6741 Grading Scheme**

Test#1: 30%
Test#2: 30%
Project: 40%

- 60% Project Report
- 20% Project Presentation (Posters)
- 10% Participation (Peer Evaluation)
- 10% Project Demo (TA's Evaluation)

### **COEN 6741: Project Report**

> max. 20 pages (w/o code) 1) Introduction 2) Instruction Set Architecture Design and Encoding 3) Datapath Design Handling Hazards 4) Control Unit Design 5) Simulation and Testing Results 6) Conclusions

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- 20% Project Presentation (Posters)
- 10% Participation (Peer Evaluation)
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# **COEN 6741: Project Presentation**

- > Poster incl. Q&A
- 1) Objective
- 2) Instruction Set Architecture Design and Encoding
- 3) Datapath Design
- 4) Control Unit Design
- 5) Simulation and Testing Results
- 6) Conclusions
- 7) Poster Quality
- 8) Answering Questions

Handling Hazards

- 60% Project Report
- 20% Project Presentation (Posters)
- 10% Participation (Peer Evaluation)
- 10% Project Demo (TA's Evaluation)



#### Detailed evaluation results for COEN6741W Winter 2017

Please note:

- Private comments for the professor will not be shown to the students.
- Comments made about individual group members are seen by their peers, but are listed anonymously.
- Only the professor can see the identity of the author of individual comments.
- Comments can be edited by the professor before the results are released to the class, after the evaluation deadline has passed.

#### A

Group Size: 4

Completed Evaluations: 0

Private comments for the professor:

#### Evaluation of Sharathsrinivasan Anthur (40040574):

ConceptualPracticalAverageEvaluatorContributionsContributionsWork EthicAverage0000

#### Evaluation of Muhammadtaha Qureshi (27741855):

ConceptualPracticalAverageEvaluatorCooperationContributionsWork EthicAcross AllAverage00000

#### Evaluation of Vivek Shah (40026089):

Evaluator	Cooperation	Conceptual Contributions	Practical Contributions	Work Ethic	Average Across All
Average	0	0	0	0	0

Evaluation of Februarcai Nutla (40020262).

# **COEN 6741: Project Peer Evaluation**

Private Comments on Team members

- Cooperation
- Conceptual Contributions
- Practical Contributions
- Work Ethic

Scores out of 7 (1 being the lowest)

Evaluations with "only" 6 and 7 will be VOID

Only average will be displayed (no individual scores)

# **COEN 6551: Project Peer Evaluation**

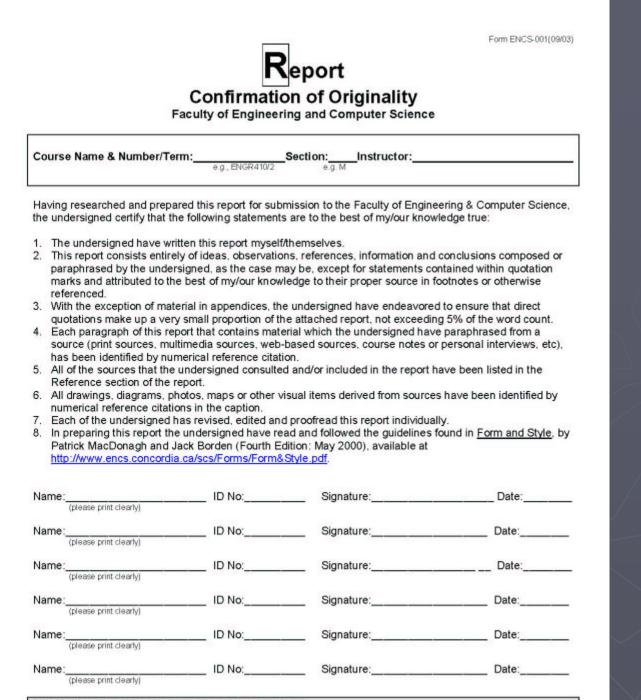
#### On-line at <u>https://pes.concordia.ca</u>

Comments made about individual group members are seen by their peers, but are listed anonymously.

Only the professor can see the identity of the author of individual comments.

Private comments for the professor will not be shown to the students.

- 60% Project Report
- 20% Project Presentation (Posters)
- 10% Participation (Peer Evaluation)
- 10% Project Demo (TA)
- 0% Confirmation of Originality (mandatory!)



Do Not Write in this Space - Reserved for Instructor

#### **COEN 6551: Project Deadlines**

 Project Reports:
 Wednesday April 11, before 5pm!
 Dr. Tahar's Mailbox in EV05.175 or ECE Dept. Secretary in EV05.139

Poster Presentations:
 Thursday April 12 @ 11.45am!
 Classroom: H-557

#### **COEN 6551: Project Deadlines**

Peer Evaluations:
 Thursday & Friday, April 12-13
 On-line at <u>https://pes.concordia.ca</u>

Project Demonstrations:
 Friday April 13 in H-811
 Time slots to be scheduled with TA

#### **COEN 6741: Test #1**

>Test#1: Thursday, February 15 @ 11.45am Classroom: H-557 Last Office Hours ▶ Monday, February 12 @ 1:30-2:30pm Topics ...

# COEN 6741: Test#1 Topics

Fundamentals of Quantitative Design & Analysis Introduction and Terminology Performance Measurements Amdahl' Law CPU Formula Instruction Set Principals: Instruction Set Architectures (Stack, Accu, GPR) Instruction Set Characteristics (6) MIPS R3000 Instruction Pipelining: MIPS Instruction Pipeline Pipeline Hazards (structural, data, control) Exceptions & Multicycle Operations MIPS R4000 Pipeline Material allowed: Only Calculators (closed book!)

#### **COEN 6741: Test #2**

►Test#2: Thursday, April 5 @ 11.45am Classroom: H-557 Last Office Hours ► Wednesday, April 4 @ 1:30-2:30pm Topics ....

### COEN 6741: Test#2 Topics

Memory Hierarchy Design

- Memory Hierarchy (Organization, Access Time)
- Cache (Organization)
- Virtual Memory (TLB)
- Main Memory
- Instruction-Level Parallelism:
  - Loop Unrolling
  - Dynamic scheduling (Tumasulo, Speculation)
  - Dynamic Branch Prediction (BHB, BTB)
  - Multiple Issue (Superscalar, VLIW, Vector)
- Thread-Level Parallelism:
  - Simultaneous Multithreading
  - Chip Level Multiprocessing

Material allowed: Only Calculators (closed book!)

# Good Luck ©